

## **Amendment to the Specification**

Please replace paragraph [0001] with the following amended paragraph.

[0001] This application is related to, and claims the benefit of, U.S. Provisional Patent Application No. 60/267,241, filed on February 6, 2001 and entitled "Line Powered ADSL Repeater with Communications, Control, and Diagnostics." This application also relates to commonly assigned U.S. Patent Application No. 09/569,470, filed on May 12, 2000 and entitled "DSL Repeater," U.S. Patent Application No. 09/610,788, filed on July 6, 2000 and entitled "DSP-Based Repeater for DSL Signals," U.S. Patent Application No. 09/670,475, filed on September 26, 2000 and entitled "Load Coil And DSL Repeater Including Same," U.S. Patent Application No. [[\_\_\_\_\_] ] 10/072,833 filed on [[\_\_\_\_\_] ] February 6, 2002 and entitled "Loop Extender with Selectable Line Termination and Equalization," and U.S. Patent Application No. [[\_\_\_\_\_] ] 10/072,091 filed on [[\_\_\_\_\_] ] February 6, 2002 and entitled "Loop Extender with Communications, Control, and Diagnostics." The disclosures of these related applications are hereby incorporated by this reference.

Please replace paragraph [0044] with the following amended paragraph.

[0044] DCP 612 receives control signals from local loop 214, processes the control signals, sends the processed control signals to AMADC 614, receives data from AMADC 614, analyzes some or all of the received data, and sends the analyzed and

unanalyzed data to COCPS 302 via LECPS 232. AMADC 614 controls the state of DSL amplification circuitry switches (not shown) via switch control lines 616, 618, 620, and 622 upon receiving the processed control signals from DCP 612. Although four switch control lines are shown, the scope of the invention includes any number of switch control lines for controlling any number of DSL amplification circuitry switches. AMADC 614 may also sample DSL signal data at locations (not shown) in the DSL amplification circuitry 605 via a plurality of diagnostic lines 624, and send the sampled data to DCP 612 for analysis. The sampling of DSL signal data via diagnostic lines 624, the control of DSL amplification circuitry via switch control lines 616, 618, 620, 622 and DSL amplification circuitry switches, and details of DSL amplification circuitry 605 and POTS loading coils 608 are disclosed in U.S. patent application Ser. No. [[ \_\_\_\_\_ ]] 10/072,091, entitled "Loop Extender with Communications, Control and Diagnostics" filed on February 6, 2002, and U.S. patent application Ser. No. [[ \_\_\_\_\_ ]] 10/072,833, entitled "Loop Extender with Selectable Line Termination and Equalization" filed on [[ \_\_\_\_\_ ]] February 6, 2002